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		DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR	
☐	L12	l5 and board$2 and transceiver$	25
☐	L11	l5 and lane$ and board$2 and transceiver$	1
☐	L10	l5 lane and board and transceiver$	1933
☐	L9	L8 and l5	67
☐	L8	375/354.ccls. or 375/358.ccls. or 375/371.ccls. or 375/373.ccls. or 375/376.ccls. or 375/219.ccls. or 375/222.ccls. or 327/141.ccls. or 327/147.ccls. or 327/144.ccls. or 327/155.ccls. or 327/154.ccls. or 327/156.ccls.	16754
☐	L7	((transmit$5 near3 clock$) near4 (control$4 or adjust$4 or synchroniz$4) near5 (receiv$5 near3 clock$)) and ((clock$ or phase$) near3 (information$ or data$)) and ((previouse$3 near3 phase$2) same (current$3 near3 phase$2))	0
☒	L6	((transmit$5 near3 clock$) near4 (control$4 or adjust$4 or synchroniz$4) near5 (receiv$5 near3 clock$)) and ((clock$ or phase$) near3 (information$ or data$)) and ((previouse$3 near3 clock$3) same (current$3 near3 clock$3))	0
☐	L5	L1 and l2	381
☐	L4	L3 and l2	30
☐	L3	((transmit$5 near3 clock$) near4 (control$4 or adjust$4 or synchroniz$4) near5 (receiv$5 near3 clock$)) same ((clock$ or phase$) near3 (information$ or data$)) same (transceiver$ or modem$)	35
☐	L2	@ad<=20040130	25467105
☐	L1	((transmit$5 near3 clock$) near4 (control$4 or adjust$4 or synchroniz$4) near5 (receiv$5 near3 clock$)) same ((clock$ or phase$) near3 (information$ or data$))	482

END OF SEARCH HISTORY

PALM INTRANET

Inventor Information for 10/813235

Inventor Name	City	State/Country
AMIRICHIMEH, ABBAS	IRVINE	CALIFORNIA
BAUMER, HOWARD	LAGUNA HILLS	CALIFORNIA
LOUIE, JOHN	HUNTINGTON BEACH	CALIFORNIA
PARTHASARATHY, VASUDEVAN	IRVINE	CALIFORNIA
YING, LINDA	IRVINE	CALIFORNIA

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PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = AMIRICHIMEH
First Name = ABBAS

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10695458	Not Issued	90	10/29/2003	CROSS LINK MULTIPLEXER BUS	AMIRICHIMEH, ABBAS
10695498	Not Issued	30	10/29/2003	Cross link multiplexer bus configured to reduce cross-talk	AMIRICHIMEH, ABBAS
10813235	Not Issued	30	03/31/2004	System and method of phase-locking a transmit clock signal phase with a receive clock signal phase	AMIRICHIMEH, ABBAS
10813363	Not Issued	41	03/31/2004	Transceiver device with a transmit clock signal phase that is phase-locked with a receiver clock signal phase	AMIRICHIMEH, ABBAS
60540295	Not Issued	159	01/30/2004	Transceiver device with a transmit clock signal phase that is phase-locked with a receive clock signal phase	AMIRICHIMEH, ABBAS

Inventor Search Completed: No Records to Display.

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Last Name	First Name	
AMIRICHIMEH	ABBAS	Search

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Inventor Name Search Result

Your Search was:

Last Name = BAUMER
First Name = HOWARD

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10454012	Not Issued	80	06/04/2003	Method and system for exploiting spare link bandwidth in a multilane communication channel	BAUMER, HOWARD
10454273	6859154	150	06/04/2003	METHOD TO OVERLAY A SECONDARY COMMUNICATION CHANNEL ONTO AN ENCODED PRIMARY COMMUNICATION CHANNEL	BAUMER, HOWARD
10665648	Not Issued	41	09/19/2003	Method and system to provide blade server load balancing using spare link bandwidth	BAUMER, HOWARD
10667385	Not Issued	30	09/23/2003	Verification and correction of 10GBASE-X lane routing between nodes	BAUMER, HOWARD
10667604	Not Issued	30	09/23/2003	Progammable Q-ordered sets for in-band link signaling	BAUMER, HOWARD
10695458	Not Issued	90	10/29/2003	CROSS LINK MULTIPLEXER BUS	BAUMER, HOWARD
10695498	Not Issued	30	10/29/2003	Cross link multiplexer bus configured to reduce cross-talk	BAUMER, HOWARD
10767729	Not Issued	41	01/30/2004	Method of monitoring the quality of a communications channel	BAUMER, HOWARD
10767748	Not Issued	41	01/30/2004	System for monitoring the quality of a communications channel with mirror receivers	BAUMER, HOWARD
10813235	Not Issued	30	03/31/2004	System and method of phase-locking a transmit clock signal phase with a receive clock signal phase	BAUMER, HOWARD
10813363	Not Issued	41	03/31/2004	Transceiver device with a transmit clock signal phase that is phase-	BAUMER, HOWARD

				locked with a receiver clock signal phase	
10829636	6911922	150	04/22/2004	METHOD TO OVERLAY A SECONDARY COMMUNICATION CHANNEL ONTO AN ENCODED PRIMARY COMMUNICATION CHANNEL	BAUMER, HOWARD
10894103	6930621	150	07/19/2004	METHOD TO OVERLAY A SECONDARY COMMUNICATION CHANNEL ONTO AN ENCODED PRIMARY COMMUNICATION CHANNEL	BAUMER, HOWARD
11799368	Not Issued	17	05/01/2007	Crosstalk emission management	BAUMER, HOWARD
60200813	Not Issued	159	04/28/2000	High-speed serial transceiver	BAUMER, HOWARD
60540295	Not Issued	159	01/30/2004	Transceiver device with a transmit clock signal phase that is phase-locked with a receive clock signal phase	BAUMER, HOWARD
60726661	Not Issued	159	10/17/2005	Apparatus and method of remote PHY auto-negotiation	BAUMER, HOWARD
60862742	Not Issued	20	10/24/2006	PHYSICAL LAYER AGGREGATION	BAUMER, HOWARD
09844266	6509773	150	04/30/2001	PHASE INTERPOLATOR DEVICE AND METHOD	BAUMER, HOWARD A.
09844283	Not Issued	93	04/30/2001	METHODS AND SYSTEMS FOR ADAPTIVE RECEIVER EQUALIZATION	BAUMER, HOWARD A.
09844296	7012983	150	04/30/2001	TIMING RECOVERY AND PHASE TRACKING SYSTEM AND METHOD	BAUMER, HOWARD A.
09844432	7016449	150	04/30/2001	TIMING RECOVERY AND FREQUENCY TRACKING SYSTEM AND METHOD	BAUMER, HOWARD A.
09844441	7058150	150	04/30/2001	HIGH-SPEED SERIAL DATA TRANSCEIVER AND RELATED METHODS	BAUMER, HOWARD A.
10346210	6791388	150	01/17/2003	PHASE INTERPOLATOR DEVICE AND METHOD	BAUMER, HOWARD A.
10681244	Not Issued	61	10/09/2003	Integrated packet bit error rate tester for 10G SERDES	BAUMER, HOWARD A.
10694729	Not	61	10/29/2003	Multipurpose and programmable	BAUMER,

	Issued			pad ring for an integrated circuit	HOWARD A.
10694730	Not Issued	30	10/29/2003	Programmable management IO pads for an integrated circuit	BAUMER, HOWARD A.
10694788	7035228	150	10/29/2003	MULTI-RATE, MULTI-PORT, GIGABIT SERDES TRANSCEIVER	BAUMER, HOWARD A.
10855392	6995594	150	05/28/2004	PHASE INTERPOLATOR DEVICE AND METHOD	BAUMER, HOWARD A.
10876602	Not Issued	41	06/28/2004	Phase interpolator based transmission clock control	BAUMER, HOWARD A.
11046238	Not Issued	20	01/31/2005	In-band access of management registers	BAUMER, HOWARD A.
11267184	Not Issued	95	11/07/2005	SCRAMBLED BLOCK ENCODER	BAUMER, HOWARD A.
11272148	Not Issued	30	11/14/2005	Apparatus and method of remote PHY auto-negotiation	BAUMER, HOWARD A.
11389506	Not Issued	41	03/27/2006	Multi-rate, multi-port, gigabit serdes transceiver	BAUMER, HOWARD A.
11446144	Not Issued	25	06/05/2006	High-speed serial data transceiver and related methods	BAUMER, HOWARD A.
60421780	Not Issued	159	10/29/2002	Multi-rate, multi-port, gigabit serdes transceiver	BAUMER, HOWARD A.
10414593	Not Issued	41	04/15/2003	System and method for supporting auto-negotiation among standards having different rates	BAUMER, HOWARD ANTHONY
60446894	Not Issued	159	02/12/2003	Method to overlay a secondary communication channel onto an encoded primary communication channel	BAUMER, HOWARD ANTHONY
60448656	Not Issued	159	02/18/2003	Method and system to provide external communication using a multi-server platform having a single switch backplane	BAUMER, HOWARD ANTHONY
60448703	Not Issued	159	02/18/2003	Method to exploit spare link bandwidth in a multi-lane communication channel	BAUMER, HOWARD ANTHONY
08844221	5861766	150	04/17/1997	MULTIMODE FREQUENCY SYNTHESIZER HAVING HIGH LOOP GAIN IN FREQUENCY SEEK MODE	BAUMER, HOWARD ANTHONY
09032220	6292912	150	02/27/1998	DISK DRIVE HAVING BUILT-IN SELF-TEST SYSTEM FOR CHARACTERIZING PERFORMANCE OF THE DRIVE	BAUMER, HOWARD ANTHONY

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_____

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Last Name	First Name	
BAUMER	HOWARD	Search

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Inventor Name Search Result

Your Search was:

Last Name = LOUIE
First Name = JOHN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10813235	Not Issued	30	03/31/2004	System and method of phase-locking a transmit clock signal phase with a receive clock signal phase	LOUIE, JOHN
10813363	Not Issued	41	03/31/2004	Transceiver device with a transmit clock signal phase that is phase-locked with a receiver clock signal phase	LOUIE, JOHN
60540295	Not Issued	159	01/30/2004	Transceiver device with a transmit clock signal phase that is phase-locked with a receive clock signal phase	LOUIE, JOHN
09643904	6542859	150	08/21/2000	METHOD FOR DESIGNING A CYCLIC SYMMETRIC STRUCTURE	LOUIE, JOHN R.
60133956	Not Issued	159	05/13/1999	DAMPED TURBINE WHEEL	LOUIE, JOHN R.

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LOUIE	JOHN	Search

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Your Search was:

Last Name = LOUIE
First Name = JOHN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10813235	Not Issued	30	03/31/2004	System and method of phase-locking a transmit clock signal phase with a receive clock signal phase	LOUIE, JOHN
10813363	Not Issued	41	03/31/2004	Transceiver device with a transmit clock signal phase that is phase-locked with a receiver clock signal phase	LOUIE, JOHN
60540295	Not Issued	159	01/30/2004	Transceiver device with a transmit clock signal phase that is phase-locked with a receive clock signal phase	LOUIE, JOHN
09643904	6542859	150	08/21/2000	METHOD FOR DESIGNING A CYCLIC SYMMETRIC STRUCTURE	LOUIE, JOHN R.
60133956	Not Issued	159	05/13/1999	DAMPED TURBINE WHEEL	LOUIE, JOHN R.

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LOUIE	JOHN	Search

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Your Search was:

Last Name = PARTHASARATHY
First Name = VASUDEVAN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09391059	7263141	150	09/07/1999	CODE MAPPING IN A TRELLIS DECODER	PARTHASARATHY, VASUDEVAN
10694945	Not Issued	71	10/29/2003	Apparatus and method for automatic polarity swap in a communications system	PARTHASARATHY, VASUDEVAN
10767729	Not Issued	41	01/30/2004	Method of monitoring the quality of a communications channel	PARTHASARATHY, VASUDEVAN
10767748	Not Issued	41	01/30/2004	System for monitoring the quality of a communications channel with mirror receivers	PARTHASARATHY, VASUDEVAN
10813235	Not Issued	30	03/31/2004	System and method of phase-locking a transmit clock signal phase with a receive clock signal phase	PARTHASARATHY, VASUDEVAN
10813363	Not Issued	41	03/31/2004	Transceiver device with a transmit clock signal phase that is phase-locked with a receiver clock signal phase	PARTHASARATHY, VASUDEVAN
11124598	Not Issued	30	05/05/2005	State based algorithm to minimize mean squared error	PARTHASARATHY, VASUDEVAN
11292610	Not Issued	30	12/02/2005	Receive equalizer with adaptive loops	PARTHASARATHY, VASUDEVAN
11837278	Not Issued	19	01/01/0001	ELECTRONIC DISPERSION COMPENSATION UTILIZING INTERLEAVED ARCHITECTURE AND CHANNEL IDENTIFICATION FOR ASSISTING TIMING RECOVERY	PARTHASARATHY, VASUDEVAN
11837301	Not Issued	19	01/01/0001	ELECTRONIC DISPERSION COMPENSATION	PARTHASARATHY, VASUDEVAN

				UTILIZING INTERLEAVED ARCHITECTURE AND CHANNEL IDENTIFICATION FOR ASSISTING TIMING RECOVERY	
60138232	Not Issued	159	06/09/1999	CODE MAPPING IN A TRELLIS DECODER	PARTHASARATHY, VASUDEVAN
60172920	Not Issued	159	12/21/1999	TRUNCATED METRIC FOR NTSC INTERFERENCE REJECTION IN THE ATSC-HDTV TRELLIS DECODER	PARTHASARATHY, VASUDEVAN
60540295	Not Issued	159	01/30/2004	Transceiver device with a transmit clock signal phase that is phase-locked with a receive clock signal phase	PARTHASARATHY, VASUDEVAN
60737292	Not Issued	159	11/15/2005	Receive equalizer with adaptive loops	PARTHASARATHY, VASUDEVAN
60840123	Not Issued	20	08/25/2006	Digital electronic dispersion compensation for multi-mode fiber	PARTHASARATHY, VASUDEVAN

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PARTHASARATHY	VASUDEVAN	Search

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Your Search was:

Last Name = YING
First Name = LINDA

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10767729	Not Issued	41	01/30/2004	Method of monitoring the quality of a communications channel	YING, LINDA
10767748	Not Issued	41	01/30/2004	System for monitoring the quality of a communications channel with mirror receivers	YING, LINDA
10813235	Not Issued	30	03/31/2004	System and method of phase-locking a transmit clock signal phase with a receive clock signal phase	YING, LINDA
10813363	Not Issued	41	03/31/2004	Transceiver device with a transmit clock signal phase that is phase-locked with a receiver clock signal phase	YING, LINDA
60540295	Not Issued	159	01/30/2004	Transceiver device with a transmit clock signal phase that is phase-locked with a receive clock signal phase	YING, LINDA

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